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Lectures

- * [i217](#) Functional Programming
 - * [i636](#) Specification and Verification of Distributed Systems
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Research

- * [The OTS/CafeOBJ Method](#): A method of modeling, specifying and verifying systems.

* [List](#) of Publications

[*International Journal Papers* (Full-paper reviewed)]

- [1] Takahiro Seino, [Kazuhiro Ogata](#) and Kokichi Futatsugi: Specification and verification of a single-track railroad signaling in CafeOBJ, IEICE TRANSACTIONS on Fundamental of Electronics, Communications and Computer Science, E84-A(6):1471-1478, IEICE, 2001.
- [2] [Kazuhiro Ogata](#) and Kokichi Futatsugi: Flaw and modification of the iKP electronic payment protocols, Information Processing Letters, 86(2): 57-62, Elsevier, 2003.
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- [4] Takahiro Seino, [Kazuhiro Ogata](#) and Kokichi Futatsugi: Mechanically Supporting Case Analysis for Verification of Distributed Systems, Journal of Pervasive Computing and Communications, 1(2): 135-145, Troubador, 2005.
- [5] Weiqiang Kong, [Kazuhiro Ogata](#) and Kokichi Futatsugi: Specification and Verification of Workflows with RBAC Mechanism and SoD Constraints, International Journal of Software Engineering and Knowledge Engineering, 17(1): 3-32, World Scientific, 2007.
- [6] [Kazuhiro Ogata](#) and Kokichi Futatsugi: Modeling and verification of real-time systems based on equations, Science of Computer Programming, 66(2): 162-180, Elsevier, 2007.
- [7] [Kazuhiro Ogata](#) and Kokichi Futatsugi: Comparison of Maude and SAL by Conducting Case Studies Model Checking a Distributed Algorithm, IEICE TRANSACTIONS on Fundamental of Electronics, Communications and Computer Science, E90-A(8): 1690-1703, 2007.
- [8] [Kazuhiro Ogata](#) and Kokichi Futatsugi: State Machines as Inductive Types, IEICE TRANSACTIONS on Fundamental of Electronics, Communications and Computer Science, E90-A(12): 2985-2988, IEICE, 2007.
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- [11] [Kazuhiro Ogata](#) and Kokichi Futatsugi: Proof Score Approach to Verification of Liveness Properties, IEICE TRANSACTIONS on Information and Systems, E91-D(12): 2804-2817, IEICE,

2008.

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- [4] Kazuhiro Ogata and Norihisa Doi: Object allocation and dynamic compilation in MultithreadSmalltalk, Proceedings of the 1994 ACM Symposium on Applied Computing (SAC 1994), ACM Press, pp.452-456 (1994)
- [5] Kazuhiro Ogata, Koichi Ohhara and Kokichi Futatsugi: TRAM: an abstract machine for order-sorted conditional term rewriting systems, Proceedings of the 8th International Conference on Rewriting Techniques and Applications (8th RTA), LNCS 1232, Springer, pp.335-338 (1997).
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